

# ACOL216: Tutorial-4

Date: February 23, 2026

1. Design a 4-to-1 multiplexer using only 2-to-1 multiplexers.
2. Implement an 8-to-1 multiplexer utilizing a combination of 4-to-1 and 2-to-1 multiplexers.
3. Design a combinational logic circuit to drive a 7-segment display for decimal representation.
4. Design a parity bit checker and a digital magnitude comparator.